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10 Number of Pages (including this page)

Date: May 18, 2005
To: Vuthe Siek - 2825
Location: United States Patent and Trademark Office
Fax No.: (703) 872-9306
From: Daniel D. Hill - 35,895
Subject: 10/624,398- Leo Mathew et al

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MESSAGE:

Enclosed herewith, please find a FORMAL AMENDMENT for filing in the below-identified application.

ALL ITEMS MARKED WITH AN "X" ARE INCLUDED:

1.	x	1 page Facsimile Cover Sheet
2.	x	9 page Amendment

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Stacie Slamm
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PLEASE GIVE THESE PAPERS TO:

EXAMINER: Vuthe Siek
GROUP ART UNIT: 2825
SERIAL NO.: 10/624,398
FILED: JULY 22, 2003
INVENTOR: LEO MATHEW ET AL

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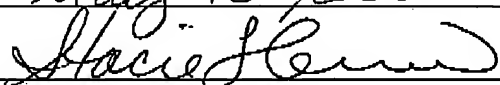
MAY 18 2005

APPLICANT(S) Leo Mathew et al GROUP ART UNIT: 2825
APPLN. NO.: 10/624,398 EXAMINER: Vuthe Siek
FILED: July 22, 2003
TITLE: METHOD FOR CONVERTING A PLANAR TRANSISTOR DESIGN
TO A VERTICAL DOUBLE GATE TRANSISTOR DESIGN

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on

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Stacie Herrera

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AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Responsive to the Office Action dated February 23, 2005, and Examiner's comments
with regard thereto, please enter the following amendments in the above-entitled application,
without prejudice or disclaimer.

If Applicant has overlooked any additional fees, or if any overpayment has been made,
the Commissioner is hereby authorized to credit or debit Deposit Account 503079.